



CONTROLLER
FPGA

Engineer: H. Nagli	LBNL 1 Cyclotron Road Berkeley, CA 94720	Size: 8 1 5 4
Drawn by: H. Nagli	TITLE: VME Board	
R3D CHK:	DOC CTRL CHK:	
MFG CTRL CHK:	DA CHK:	
REV 1	REV 1	
Time: 5:47:37 pm	Drawing Number:	Page: 1

2x 18x 256K SRAM 2x 18x 256K SRAM 2x 18x 256K SRAM 2x 18x 256K SRAM
 Changed by: naggi Date Changed: Tuesday, April 20, 1999