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| Engineer: | Lawrence Berkeley National Laboratory | |
| Drawn by: | 1 Cyclotron Road | |
| H. Nagai: | Berkeley, CA 94720 | |
| R&D CHK: | TITLE: | S280 |
| DOC CTRL CHK: | Sheet 3 of | 8154 |
| PRG CTRL CHK: | VHDL Code for FPGA Internal FIFO's | |