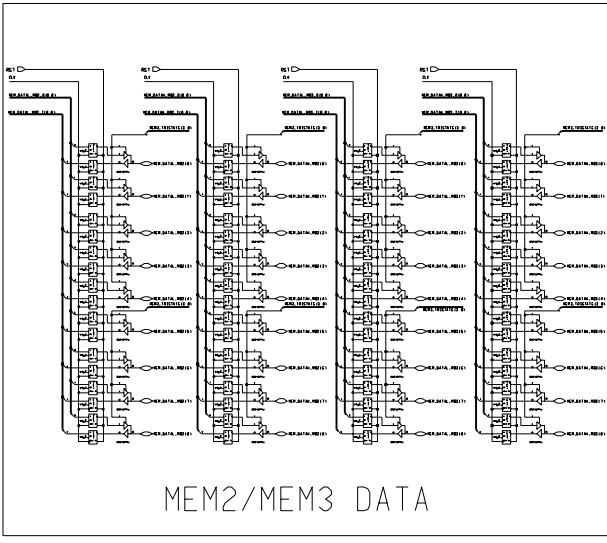
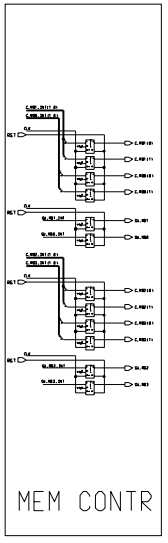


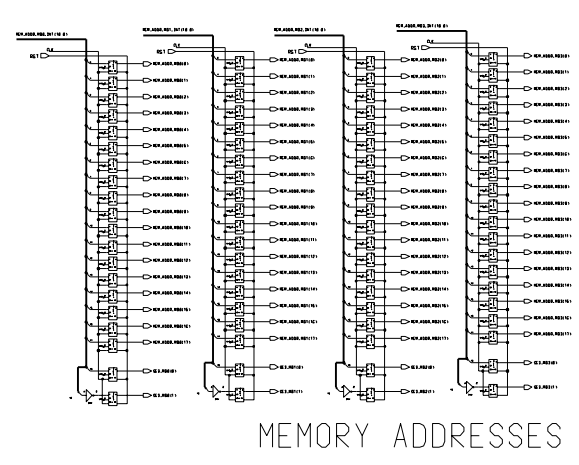
MEMO/MEM1 DATA



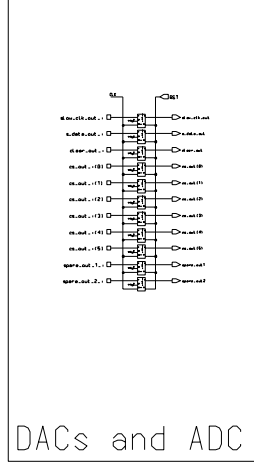
MEM2/MEM3 DATA



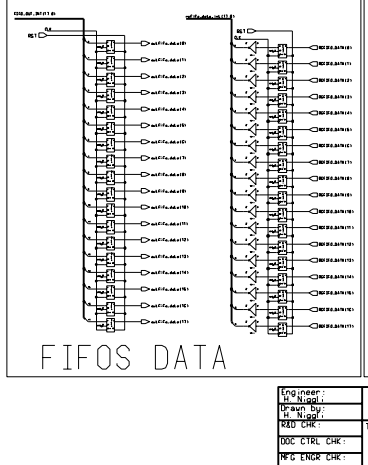
MEM CONTR



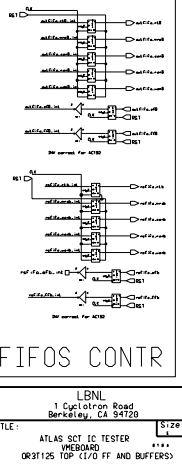
MEMORY ADDRESSES



DACs and ADC



FIFOS DATA



FIFOS CONTR

Eng: [ ] H: [ ] Drawn By: [ ] H: [ ] REV: [ ]	LBNL 1 Cyclotron Road Berkeley, CA 94720	S: 28
DOC CTRL CHK: [ ] REV ENGR CHK: [ ]	TITLE: ATLAS SCT IC TESTER VMEBOARD OR31125 TOP (C/D FF AND BUFFERS)	11/28