

### **List of Switches setting on VME board**

1. SW1: is for setting the configuration of loop & output divider of the clock synthesizer chip on VME board.

- SW1-1 to 6 = OFF.
- SW1-7 = ON.
- SW1-8 to 10 = OFF.
- SW1-11 to 12 = ON.

2. SW2: is for setting the address of VME board

- SW2-1 to 4 = OFF (not used)
- SW2-5 to 6 = OFF. ( Board\_Addr 0 to 1)
- SW2-7 to 10 = ON. ( Board\_Addr 2 to 5)
- SW2-11 to 12 = OFF. ( Board\_Addr 6 to 7)

3. SW3: this push button switch is for reset the FPGA on VME board .

4. SW4: This switch is for selecting the system clock to the FPGA chip from the on board 40 MHz oscillator or external oscillator.

\*Notes: Always turn all of them OFF before turn the select one to ON

- SW4-1 = ON to select external clock
- SW4-2 = ON to select on board 40 MHz clock ( default position )

5. SW5 This switch is for select the fast clock to control the output FIFO and the clock to the ABCD chip

\* Notes: Always turn all of them OFF before turn the select one to ON

- SW5-1&2 = ON to select on board 40 MHz clock ( default position)
- SW5-3&4 = ON to select the on board synthesizer clock