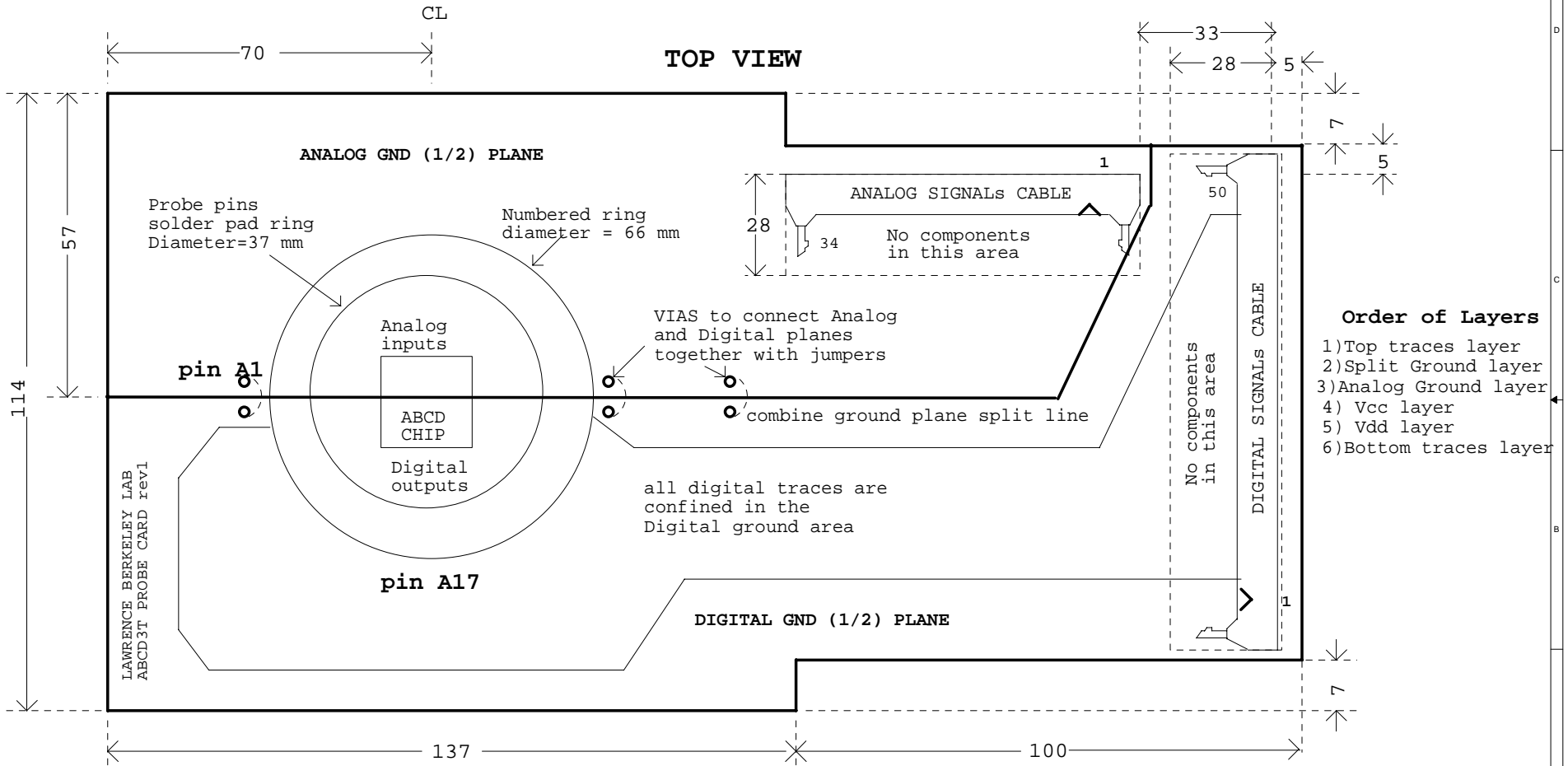


# ABCD PROBE CARD LAYOUT

## TOP VIEW



### Order of Layers

- 1) Top traces layer
- 2) Split Ground layer
- 3) Analog Ground layer
- 4) Vcc layer
- 5) Vdd layer
- 6) Bottom traces layer

### NOTES:

- 1) All dimensions are in millimeters
- 2) All the decoupling capacitor on Vcc & Vdd to ground are located inside the PAD ring on top layer and connected to the probe pin with vias.

- 3) All ground probe pins are connected direct to ground plane with vias

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ABCD PROBE CARD LAYOUT	
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