

List of modifications on the Existing standard Probe card (series number E128-RCT 50 ohm) from Circuit probe engineering

1. The new probe card will have dimension as shown on layout.
2. One 50 pins and one 34 pins 3M connectors with pin 1 position as shown on schematic will replace the 128 pins edge connector.
3. Differential pair signals will be layout in parallel and on the same trace layer.
4. All inductors will be located close to the connectors.
5. All the decoupling capacitors will be located as close as possible to the probe pins.
6. All resistors will be located around 1" from the probe pins solder pads.
7. The 50 pin connector will be position on the center of the probe card as shown.
8. The probe card will have 6 layers (50 ohm matched impedance ,full body gold) in the following order:
 - 8.1. Top traces layer.
 - 8.2. Vdd layer.
 - 8.3. Digital and Analog layer spit plane layer.(this plane will have jumpers located along the split line to allow for connect the 2 half planes together)
 - 8.4. Analog ground layer (this plane will connect to the analog half plane)
 - 8.5. Vcc layer.
 - 8.6. Bottom traces layer.
9. Unused traces will be connected direct to Digital or analog ground.